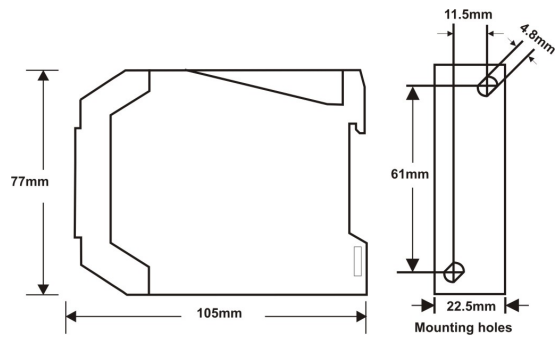


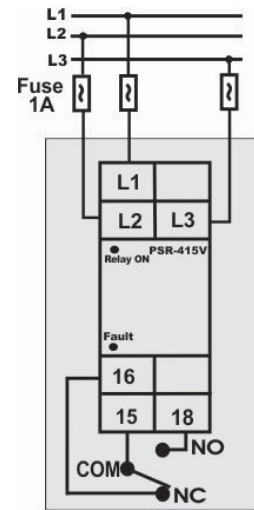
PHASE SEQUENCE RELAY



Dimensions

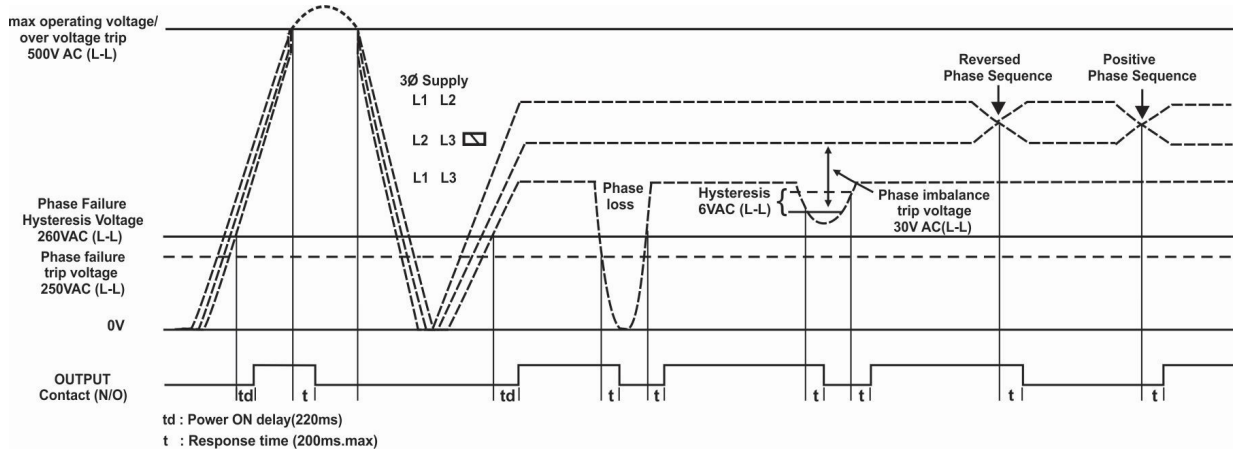
All dimensions in mm

FEATURES	<ul style="list-style-type: none"> 3Ø / 3 wire input Monitors phase sequence, phase failure, phase imbalance High reliability Led indication : Relay, Fault Compact Din rail / base mounting 		LED INDICATION CHART		
	CONDITION	RELAY	FAULT LED		
	Nominal	ON	OFF		
	Phase Reversed	OFF	ON		
	L-L Voltage > 500V or < 250V AC(L-L)	OFF	Blinking at 5Hz		
	Phase Imbalance > 30V AC(L-L)	OFF	Blinking at 2Hz		
FUNCTIONAL SPECIFICATIONS	Phase Failure Trip Voltage	< 250V AC (L-L)			
	Over Voltage Trip	> 500V AC(L-L)			
	Phase Imbalance Voltage	30V AC(L-L) Fixed			
	Phase Imbalance Hysteresis Voltage	6V AC(L-L)			
	Trip Accuracy	±10V AC(L-L)			
	Response Time	200mSec. max.			
	Power On Delay	220mSec.			
AUXILIARY SUPPLY SPECIFICATIONS	SUPPLY VOLTAGE RANGE	250 to 500V AC(L-L), 50Hz (±10%) 3Ø / 3 Wire (Recommended fuse : 1A, 230V AC fast blow)			
	NOMINAL AUXILIARY VOLTAGE	415V AC(L-L)			
	POWER CONSUMPTION	20VA Max			
ISOLATION	Supply terminals – Contact : 1.5KV, 1min				
OUTPUT SPECIFICATION	OUTPUT CONTACT	SPDT (1 C/O)			
	CONTACT RATING	NO : 5A, NC : 3A @ 250V AC, Resistive			
ACCURACY	Setting: ± 3% of full scale				
RESET	On interruption of power, On removal of fault condition				
RELAY ENDURANCE	MECHANICAL	10,000,000 ops. Minimum (1800 ops./h)			
	ELECTRICAL	100,000 ops. Minimum (1200 ops./h)			
AMBIENT CONDITIONS	OPERATING AMBIENT	0~55°C, 85% RH			
	STORAGE AMBIENT	-20°C to 85°C			
	HUMIDITY	95% RH non condensing			
GENERAL SPECIFICATIONS	TERMINATIONS	Screw type, for 2.5mm sq. wire			
	MOUNTING	DIN rail / Base mount			
	WEIGHT	120 gms			
	PROTECTION LEVEL	IP40 for housing, IP20 for terminals, Finger safe terminals			
ORDERING INFORMATION	PSR - 415V				
	DESIGN NO.	SUPPLY VOLTAGE			
	PSR	415V AC(L-L), 50Hz			



TERMINAL CONNECTIONS

TIMING DIAGRAM



Note : Unit will not detect phase sequence & phase loss until recovery from phase imbalance mode and fault LED will keep on blinking at 2Hz until nominal phase symmetry occurs.